

Working Group 10.5 (Design and Engineering of Electronic Systems)
Yearly report for the period August 2023 – August 2024
Written by Graziano Pravadelli

The WG 10.5 home page is located at: <https://cisd.di.univr.it/wg10-5/>

1 - Members

One new member has been added during the period:

- Prof. Lutfi Albasha from University of Sharjah, UAE

Then, there are in total 57 members and 12 honorary members.

The geographic distribution of the members is as follows:

- Europe: 36 members, 4 honorary members
- North America: 8 members, 5 honorary members
- South America: 3 members, 1 honorary member
- Asia 10 members, 2 honorary members
- Africa 0 members
- Oceania 0 members

2 - Officers

Since October 2020 the WG chair is Prof. Graziano Pravadelli, University of Verona, Italy

Since March 2021 the WG vice chairs are:

- Prof. Ibrahim Elfadel, Masdar Institute, United Arab Emirates
- Prof. Ricardo Reis, Universidade Federal do Rio Grande do Sul, Brazil
- Dr. Victor Grimblatt, Synopsys, Chile
- Prof. Tiziana Margaria, University of Limerick, Ireland

Their roles of the WG chairs are as follows:

Name	Geographical area	Main expertise	Promotional role
Tiziana Margaria	Europe	Formal methods	Women enrolment
Ibrahim Elfadel	Africa, Asia and Far East	Cyber physical systems and IoT	Mentoring for VLSI-SOC organizers
Ricardo Reis	America	Physical design and testing	Networking with IFIP initiatives
Victor Grimblatt	Worldwide	EDA, electronic industry	Industrial liaison

3 - Meetings

The following business meetings took place in the period August 2023 to July 2024:

- October 15, 2023, hybrid, co-located with VLSI-SOC 23 in Sharjah (UAE), 14 attendees.

- March 25, 2024, hybrid, co-located with DATE 24, 22 attendees.

All meeting minutes are available at: <https://cisdi.di.univr.it/wg10-5/meetings.html>

The following meetings are currently planned for the coming year:

- October 06, physical meeting in Tangier (Morocco) during VLSI-SOC 2024, with possibility of remotely connect for people that will not attend the conference.

4 - Sponsored Events

The main conference sponsorship activity of WG 10.5 is the annual IFIP/IEEE International Conference on Very Large Scale Integration, VLSI-SOC Conference.

The members of the Steering Committee are:

Matthew Guthaus (UC Santa Cruz, USA), Luis Miguel Silveira (INESC ID, PT), Chi-Ying Tsui (HKUST, Hong Kong, CN), Manfred Glesner (TU Darmstadt, DE), Ibrahim Elfadel (Masdar Institute of Science and Technology, UAE), Fatih Ugurdag (Ozyegin University, TR), Ricardo Reis (UFRGS, BR), Ian O'Connor (ECL, FR), Masahiro Fujita (The University of Tokyo, JP), Salvador Mir (TIMA, FR), Graziano Pravadelli (University of Verona, IT)

VLSI-SoC 2023

The 31th VLSI-SOC conference from October 16 to 18, 2023, was co-sponsored by IFIP, IEEE CEDA, IEEE CASS, Cadence, Rohde Schwarz, American University of Sharjah, Khalifa University, University of Sharjah.

Its venue was Sharjah, UAE in presence.

The conference theme was "Innovation for Trustworthy AI". The Conference was organized and chaired by Fadi Aloul, American University of Sharjah, UAE, and Luis Miguel Silveira, INESC ID, Portugal. The Program co-chairs were Prof. Lutfi Albasha, American University of Sharjah, UAE, and Prof. Abe Elfadel, Khalifa University, UAE.

There were 1 workshop by Cadence, 4 tutorials, 5 keynotes, 7 regular sessions with 35 technical presentations, 1 poster sessions with 8 contributions, 1 PhD forum with 8 presentations, 1 student forum with 7 presentations, and 4 special sessions with 8 contributions.

Events co-sponsored by WG 10.5 without financial commitment

- **SBCCI 2024** - 37th Symposium on Integrated Circuits and Systems Design, September 2-6, 2024, Paraiba, Brazil.
- **FDL 2024** – Forum on Specification and Design Languages, September 4-6, 2024, Stockholm, Sweden.

Planned Events

The following main sponsored events are already planned:

- **IFIP/IEEE VLSI-SoC 2024:** Tangier, September, 5-9 October 2024, General Chairs: Prof. Said Hamdioui (TU Delft, NL), Anass El Haddadi (ENSA, Morocco).
- **IFIP/IEEE VLSI-SoC 2025:** Puerto Varas, Chile, October 2025, General Chairs: Dr. Victor Grimblatt (Synopsys, Chile) and Prof. PE Gaillardon (University of Utha, USA)
- **IFIP/IEEE VLSI-SoC 2026:** Limassol, Cyprus, October 2026, General Chairs: Prof. Tiziana Margaria (University of Limerik, Ireland) and Prof. Ian O'Connor (Ecole Polytechnique of Lyon, France)

The following events are actually considered to be sponsored without any financial implication:

- **SBCCI 2025:** August, 2025.
- **FLD 2025:** Kaiserslautern, September, 2025

5 – Other initiatives

The WG members have completed the mapping of the WG competences in 12 topics. The result is available at the following web page: <https://cisdi.di.univr.it/wg10-5/competences.html>